



Docket No.: 01-S-015
Applicant: Semir S. Haddad et al.
Application No.: 09/943,793
Filing Date: August 31, 2001

REPLACEMENT

The diagram illustrates a system architecture labeled "FIG. 1 (PRIOR ART)". It consists of four rectangular blocks arranged horizontally. From left to right:

- AUDIO ENCODER**: Associated with terminal 105.
- VIDEO ENCODER**: Associated with terminal 110.
- OTHER DATA**: Associated with terminal 115.
- SYSTEMS DATA**: Associated with terminal 120.

Each encoder has an output line leading to a multiplexer. The multiplexer has two outputs: one labeled "AUDIO PACKETS" leading to terminal 105, and another labeled "MPEG2 STREAM" leading to terminal 100.

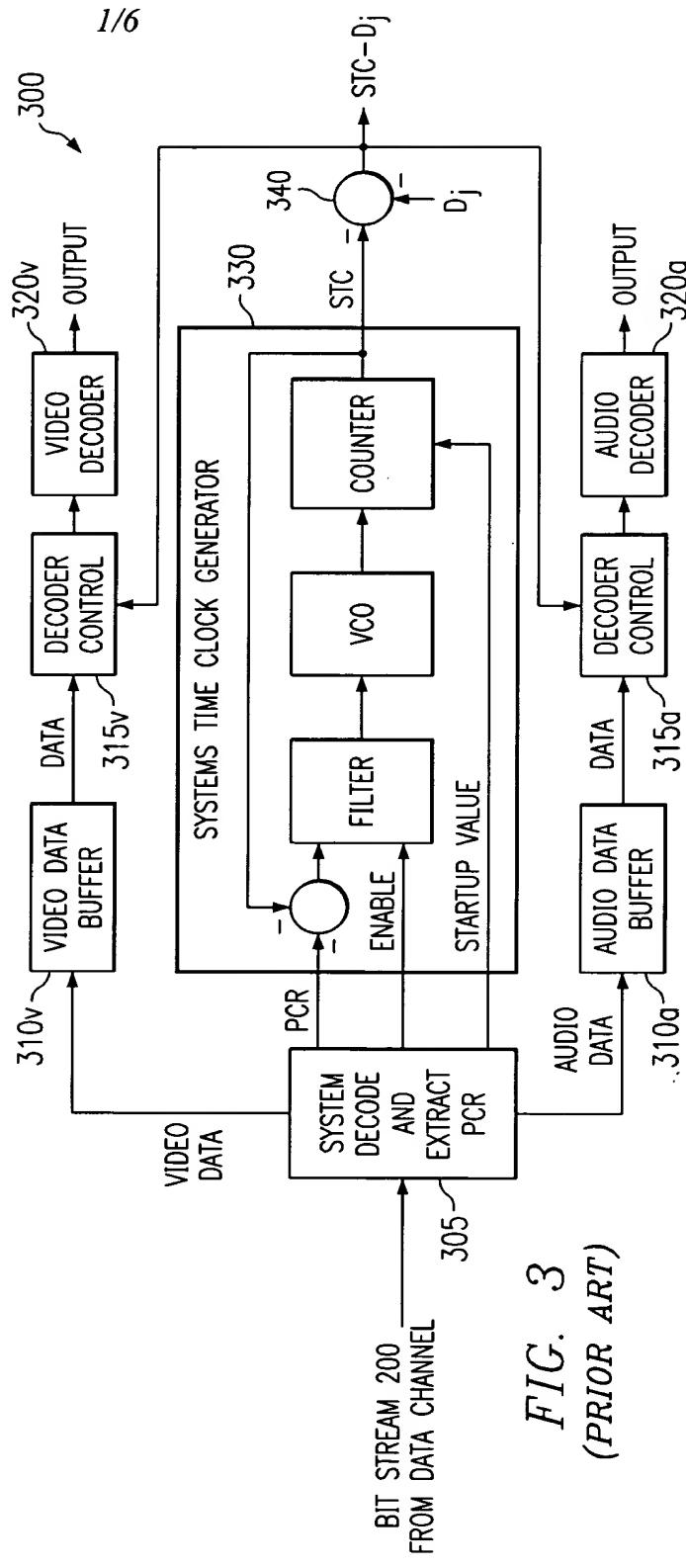
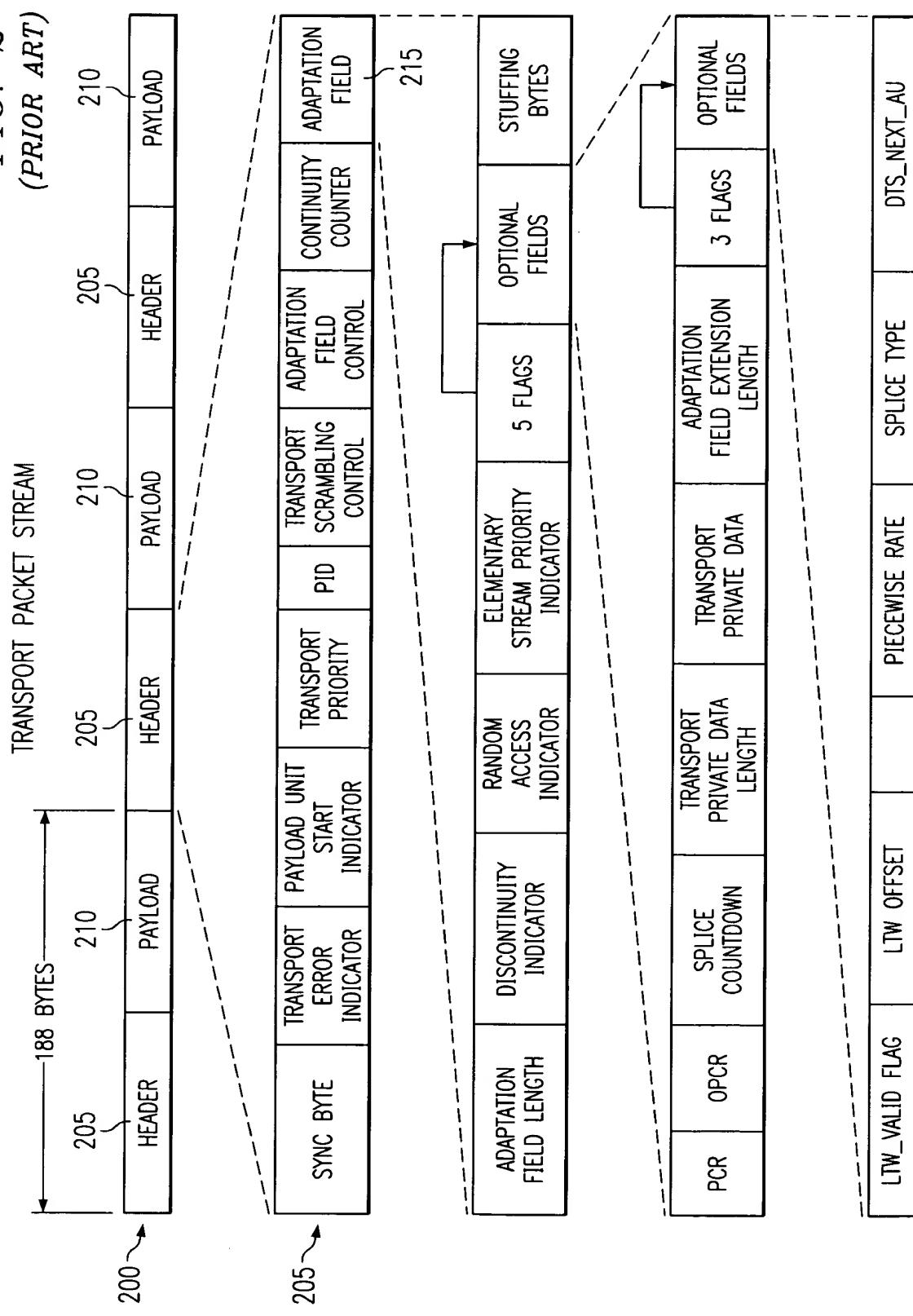


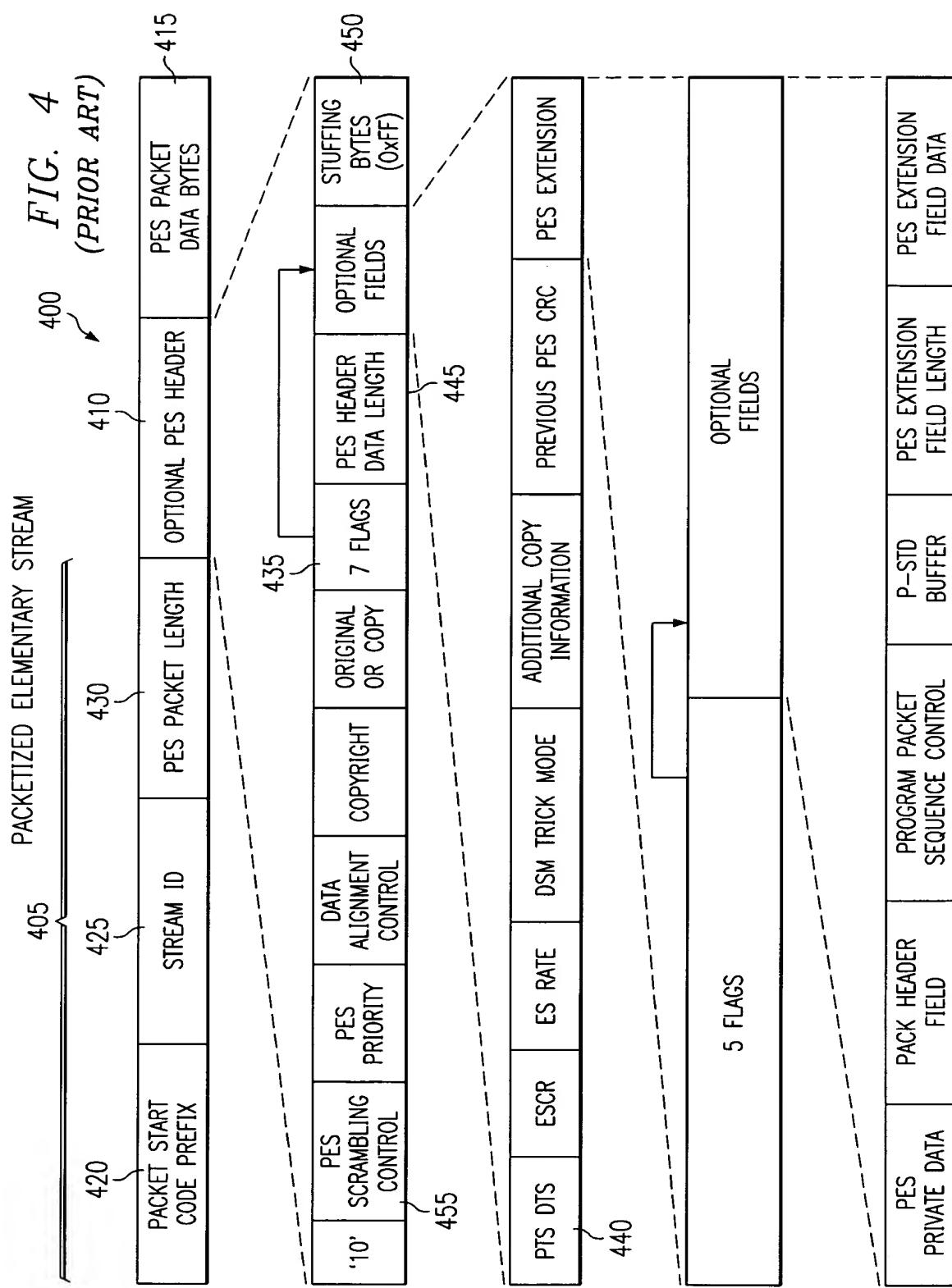
FIG. 3
(PRIOR ART)

FIG. 2
 (PRIOR ART)



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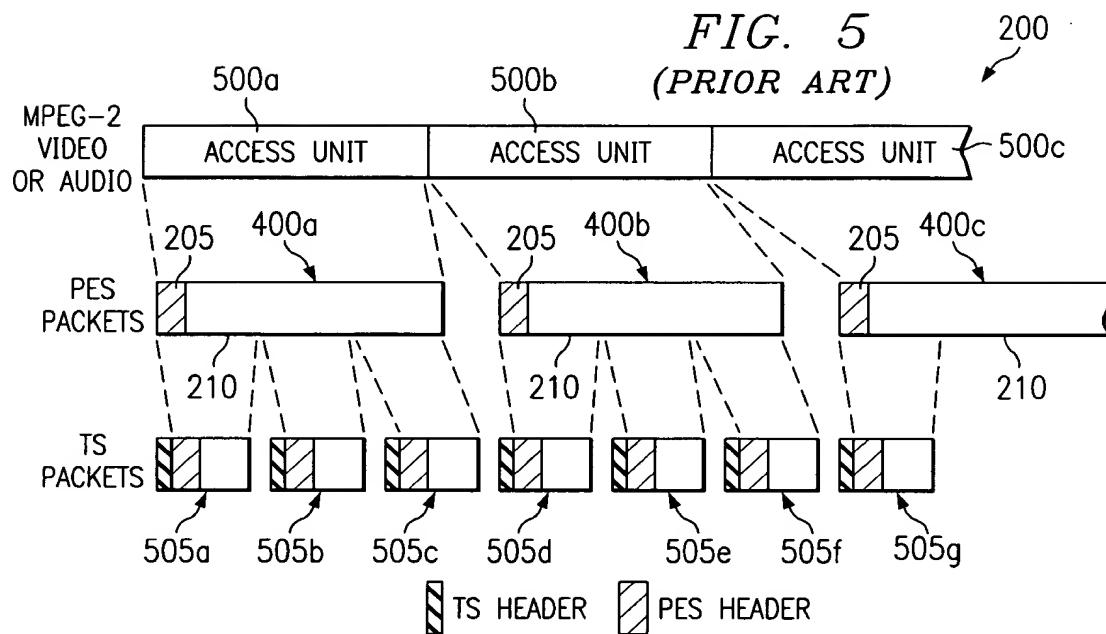


FIG. 6

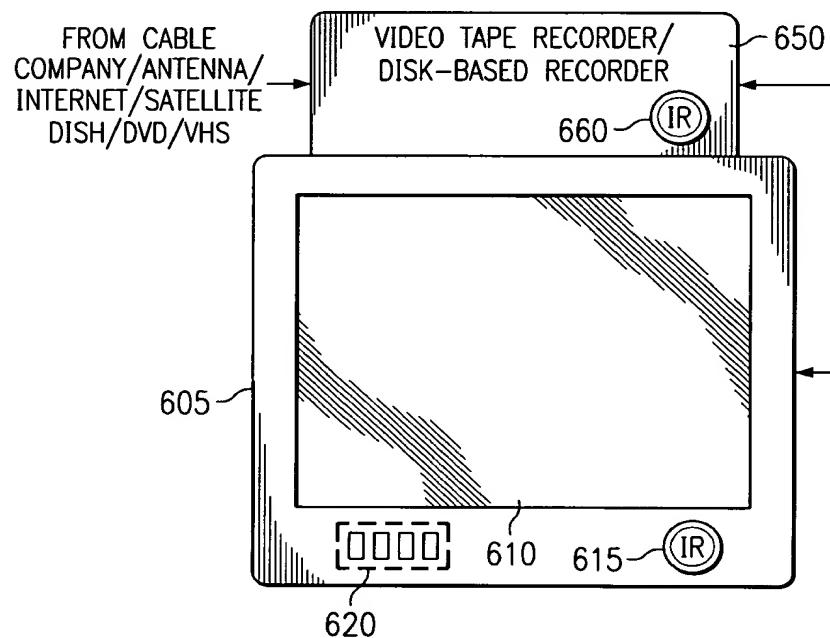


FIG. 7 5/6

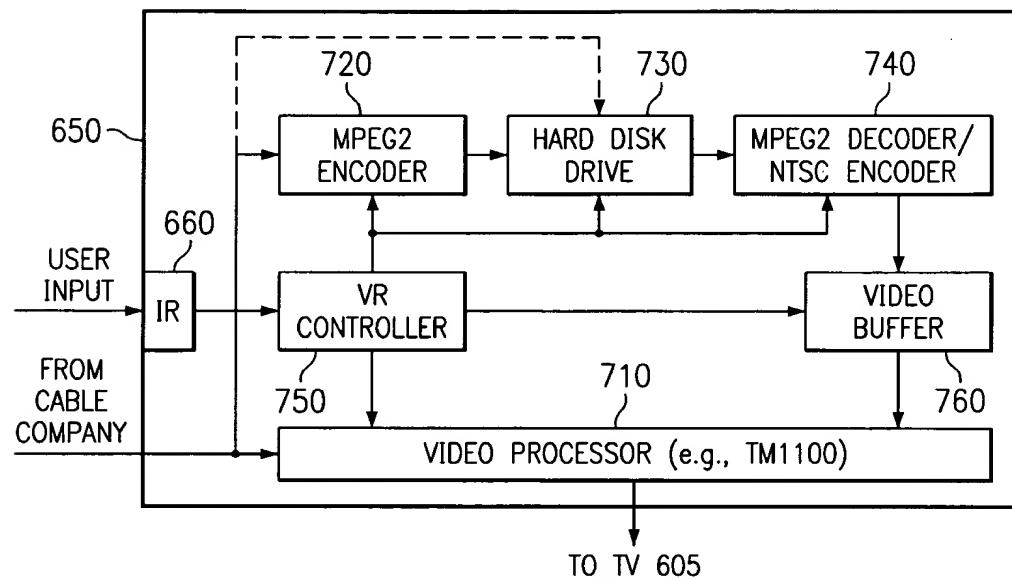
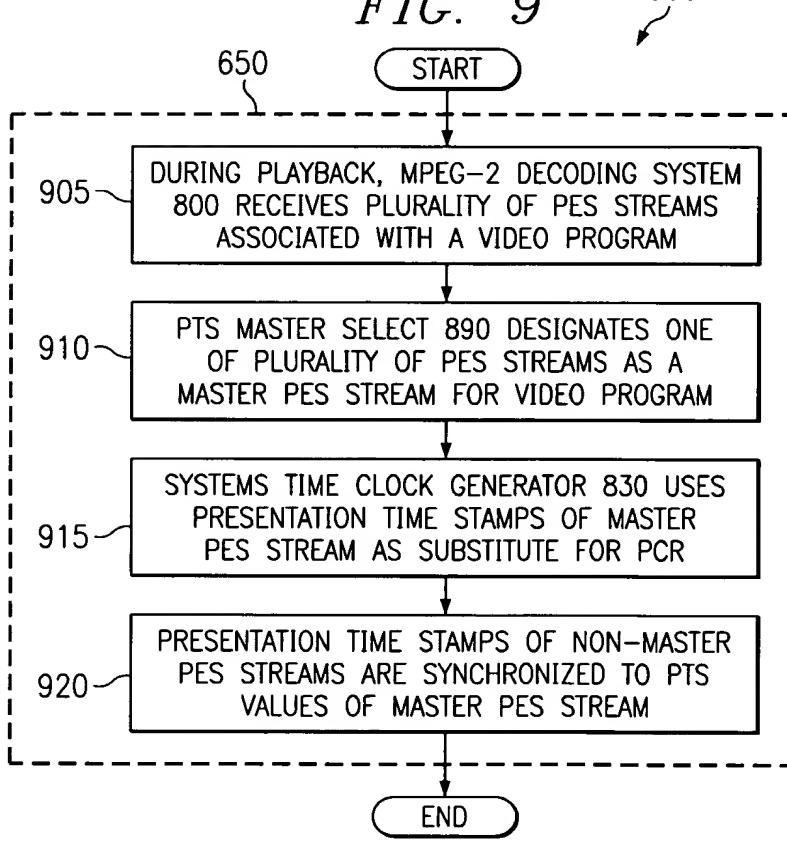


FIG. 9 900



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